

CHANNEL 1

CHANNEL 2

CHANNEL 3

CHANNEL 4

BIT

S1 D1
 S2 LPT
 S3 CAL
 D5 LPA
 NORM

CLOSE
 S6 LOOP
 OPEN

2
 FULL SCALE

GO RATE

CLOCK

2 1
 S4 PHA

8 16 32 64 128
 4 2
 MAN SWEEPS PER BIT

1
 DC OFFSET

KILL ERROR
 S8 KILL DC
 MAN STEP
 S9 FREE RUN
 S10 STEP

DBM-255-NMB

+8V -6.4V VERT. V_{OUT}

TRIG CLK

ON PWR

HORIZ.

GO A B DATA O.T. BUSY

A-7 PHA TEST SET
PANEL DETAIL

18 JULY 75
AFH