



B — +8V  
 S — +5V  
 K — -6.4V  
 Z —

**DIGITAL TO ANALOG  
ERROR DETECTOR**

J — FEEDBACK  
 K — ERROR  
 L — OUTPUT

10005  
 100s  
 5082-7302  
 10s  
 5082-7302  
 TO  
 LSD  
 5082-7302

**BINARY --> BCD CONVERTER**  
 T<sup>2</sup>L - 74185 DIP, ALL:16 TO +5V  
 15 & 8 TO GND

N CONNECTOR PINS

NOTE: ON CD4023s, 14 TO +8V  
 7 TO GND  
 CD4049, 1 TO +8V (U1,U13), +5V(U16, U17)  
 8 TO GND

10A  
 13B  
 12C  
 11D  
 TEST BIT  
 SELECT

**A-4**  
**MJS - PHA TEST SET**  
**DATA STORE, ERROR SENSOR**  
**& BCD CONVERSION**  
 21 FEB 75